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S/N 10/521908

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TSUKADA	Examiner:	HOANG, Tu Ba
Serial No.:	10/521908	Group Art Unit:	2832
Filed:	January 20, 2005	Docket No.:	10921.0269USWO
Title:	CHIP RESISTOR AND MANUFACTURING METHOD THEREFOR		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on December 15, 2006.

By: 

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Dear Sir:

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PATENT TRADEMARK OFFICE

Dated: December 15, 2006

Respectfully submitted,

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